

Remark RFQ – Seller CMMC Level Affirmation {Applies to MIL RFQs} 24AUG2026

(a) Seller affirms, in accordance with DFARS 252.204-7021 for award of any order resulting from this Request For Quote (RFQ), that the Seller has, for each of the Seller's information systems that will process, store, or transmit FCI or CUI and that will be used in performance of a contract resulting from this RFQ—

(i) The current CMMC status entered in the Supplier Performance Risk System (SPRS) (<https://piee.eb.mil>) at the CMMC level as provided by Supplier in the Remark C56 - GE Aerospace Representation and Certification response; and

(ii) A current affirmation of continuous compliance with the security requirements identified at 32 CFR part 170 in SPRS.

(b) Seller further agrees to provide evidence within its proposal via screenshot of the SPRS system that represents the above certification

(c) Plan of action and milestones. If the Seller has a CMMC Status of Conditional, the Seller shall successfully close out a valid plan of action and milestones (32 CFR 170.21) to achieve a CMMC Status of Final. Seller shall provide Purchaser evidence via screenshot of the SPRS system within 30 days of closure. Seller shall promptly notify Purchaser if Seller fails to achieve a CMMC status of Final by the expiration of its plan of action and milestones date.

(d) CMMC unique identifiers (CMMC UIDs). The Seller shall provide, in the proposal, the CMMC UIDs issued by SPRS for each contractor information system that will process, store, or transmit FCI or CUI during performance of a contract, task order, or delivery order resulting from this RFQ. The Seller also shall update the list when new CMMC UIDs are generated in SPRS. The CMMC UIDs are provided in SPRS after the Seller enters the results of self-assessment(s) for each such information system.

(e) The Seller will not be eligible for award resulting from this RFQ if each of the Seller's contractor information systems that will process, store, or transmit FCI or CUI and that will be used in performance of any subsequent order do not meet these requirements.